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PTO/SB/21 (09-04)

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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	10/730,333
	Filing Date	12-08-2003
	First Named Inventor	WARREN M. FARNWORTH
	Art Unit	2822
	Examiner Name	POTTER, ROY
Total Number of Pages in This Submission	Attorney Docket Number	02-1500.01

ENCLOSURES (Check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm Name	LAW OFFICE OF STEPHEN A. GRATTON		
Signature			
Printed name	Stephen A. Gratton		
Date	January 6, 2006	Reg. No.	28,418

CERTIFICATE OF TRANSMISSION/MAILING	
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:	
Signature	
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Date	1-6-2006

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:



WARREN M. FARNWORTH  
ALAN G. WOOD  
WILLIAM M. HIATT  
JAMES M. WARK  
DAVID R. HEMBREE  
KYLE K. KIRBY  
PETE A. BENSON

ART UNIT: 2822

EXAMINER: POTTER, ROY KARL

Serial No.: 10/730,333

Filing Date: 10/08/2003

For: MULTI CHIP SCALE SEMICONDUCTOR COMPONENTS  
AND WAFER LEVEL METHODS OF FABRICATION

Docket No.: 02-1500.1

**COMMUNICATION TO THE EXAMINER AFTER  
NOTICE OF ALLOWANCE UNDER 37 CFR §1.312**

**January 6, 2006**

Mail Stop ISSUE FEE  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

This communication is being filed after the Notice of Allowance dated January 3, 2006.

Information Disclosure Statements were filed on May 24, 2004, January 31, 2005 and October 24, 2005 in the above application. Applicants have not received initialed copies of PTO Form SB/08 indicating these references were considered.

Attached to this communication are copies of the filing documents for the Information Disclosure Statements filed on May 24, 2004, January 31, 2005

and October 24, 2005 (Transmittal Form, the Information Disclosure Statement, and PTO Form SB/08). In addition, copies of the return receipt postcards are included acknowledging receipt of filing the above papers by the Patent Office.

It is requested that these references be made of record and an initialed copy of PTO Form SB/08 be provided to Applicant.

Dated this 6th day of January, 2006.

Respectfully submitted:




Stephen A. Grafton  
Registration No. 28,418  
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CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Mail Stop ISSUE FEE, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on this 6th day of January, 2006.

January 6, 2006  
Date of Signature

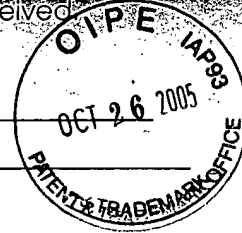
  
Stephen A. Grafton, Attorney of Record



Docket No. 02-1500.01 Serial No. 10/730,333  
Date of deposit: 10-24-2005

The date stamp of the Patent Office hereon may be considered as the date on which papers indicated below were received

- ☒ Transmittal Form
- ☐ Fee Transmittal Form ☐ Check(s)
- ☐ Amendment/Response to the Office Action dated
- ☐ A Petition for Extension of Time
- ☒ An Information Disclosure Statement
- ☒ PTO Form SB/08A ☐ Copies of cited references
- ☐ Drawings
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Docket No. 02-1500.1 Serial No. 10/730,333  
 Date of Deposit: May 24, 2003

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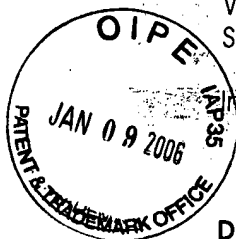
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Patent Application Serial No. 10/730,333

WAFER LEVEL METHOD FOR FABRICATING MULTI-DICE CHIP SCALE  
SEMICONDUCTOR COMPONENTS (as amended)



Inventor(s): WARREN M. FARNWORTH, ALAN G. WOOD, WILLIAM  
M. HIATT, JAMES M. WARK, DAVID R. HEMBREE  
KYLE K. KIRBY, PETE A. BENSON

DOCKET NO. 02-1500.1

MAIL STOP AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450



Sir:

THE RETURN OF THIS PROPERLY STAMPED WILL ACKNOWLEDGE RECEIPT OF:

1. Transmittal Form
2. A Preliminary Amendment, pgs 1-13
3. An Information Disclosure Statement with a copy of the cited reference

Attorney: Stephen A. Gratton  
Mailed: January 31, 2005

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